

FIG.1(Prior Art)

Title: METHOD AND CIRCUIT FOR
GENERATING A TRACKING ERROR SIGNAL
USING DIFFERENTIAL PHASE DETECTION
Inventor: LAI, et al.
Appln. No.: Unknown
Docket No.: 025796-00011

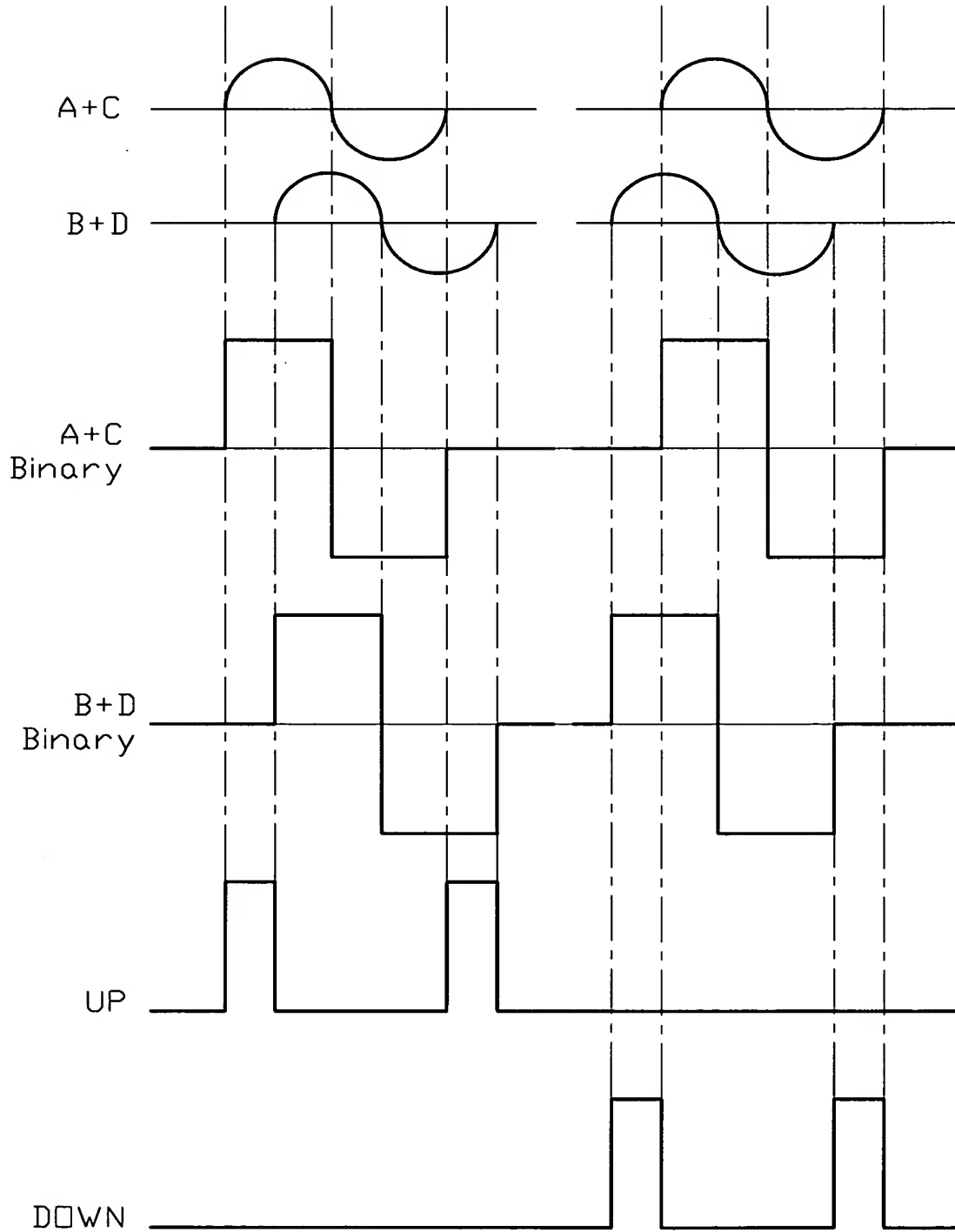


FIG.2(Prior Art)

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Inventor: LAI, et al.
Appln. No.: Unknown
Docket No.: 025796-00011

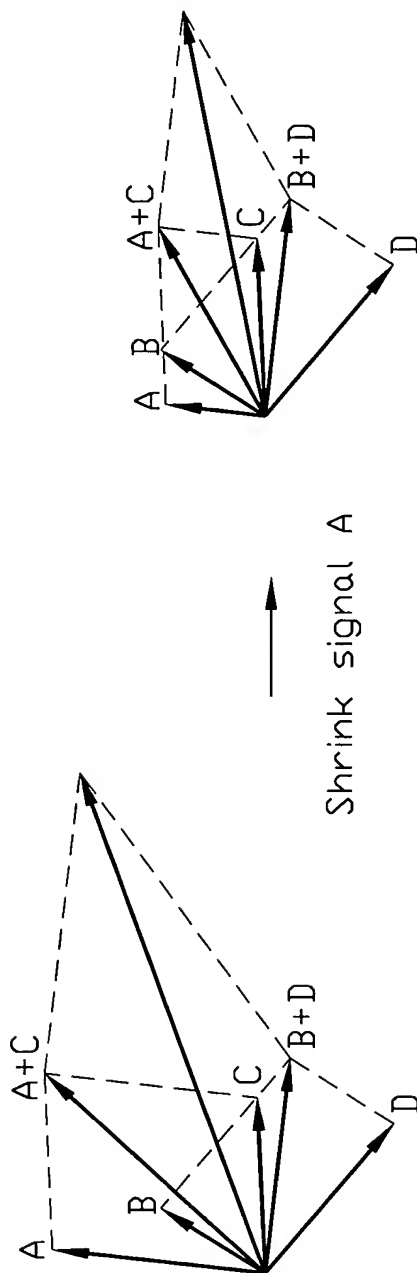


FIG.3(Prior Art)

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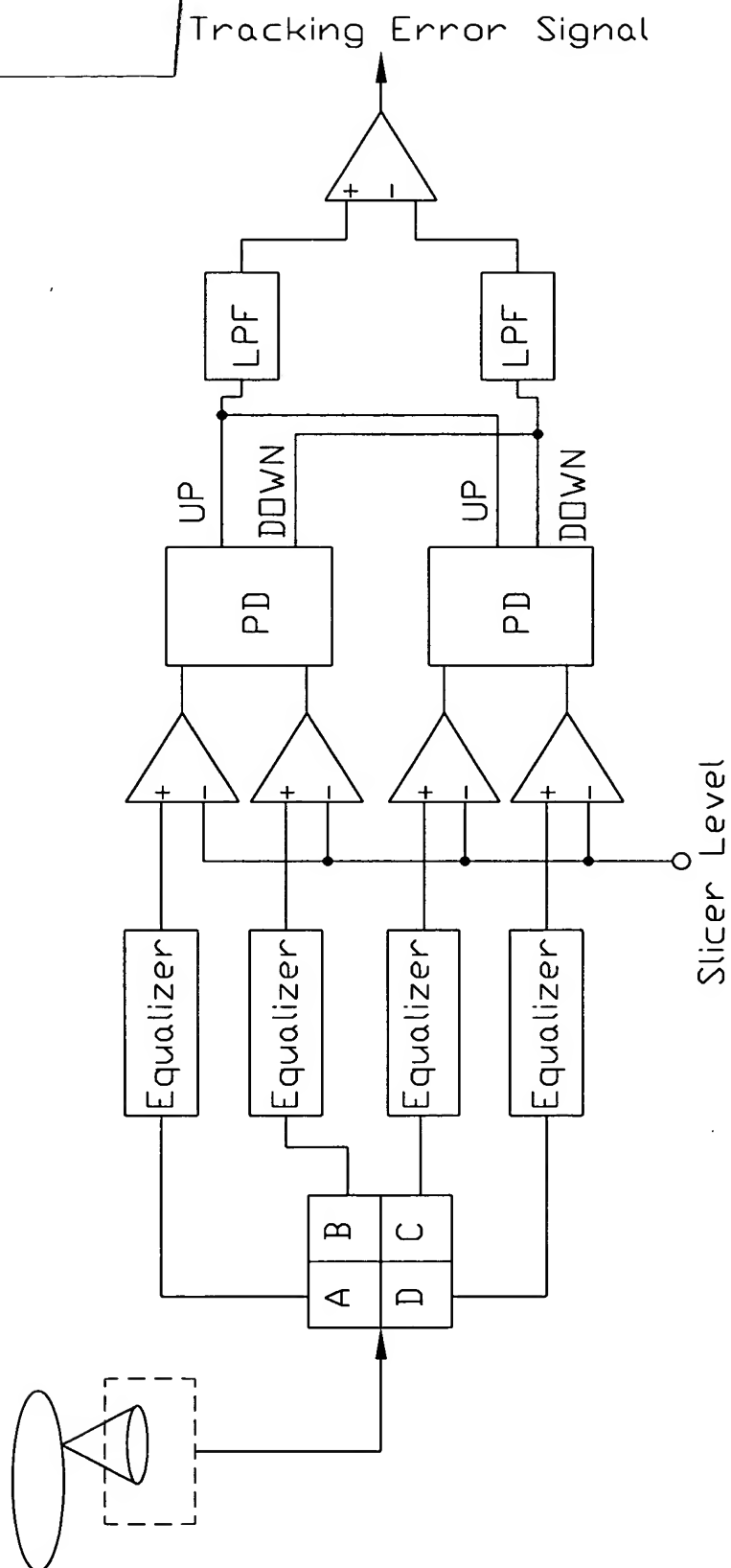


FIG.4(Prior Art)

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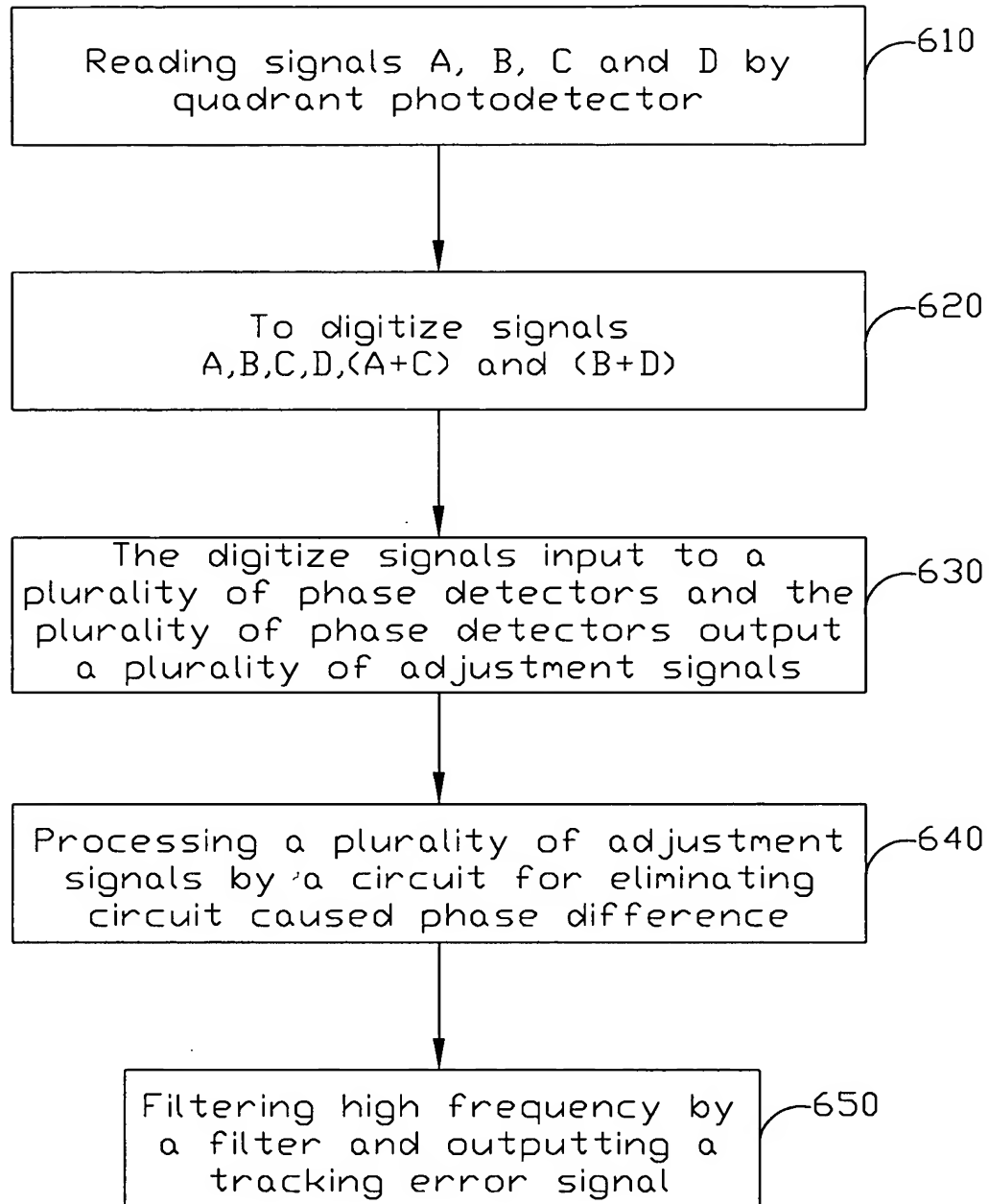


FIG.5

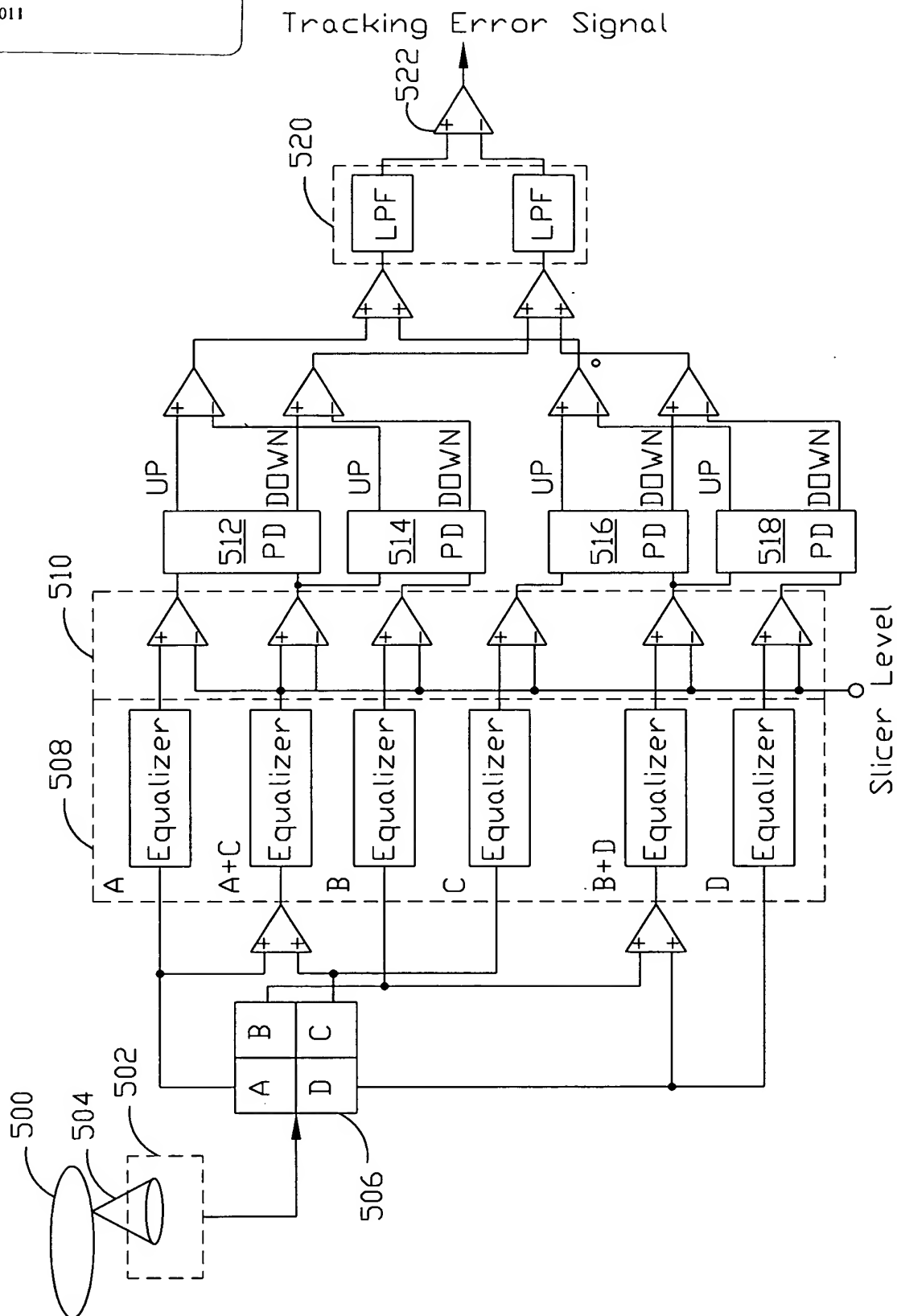


FIG.6